


Form PTO-1449 OF COMMERCE OFFICE	U.S. DEPARTMENT PATENT AND TRADEMARK INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	ATTY. DOCKET NO. YOR9-2001-0606 (8728-545)	SERIAL NO. Unassigned 
		APPLICANT Michael K. Gschwind, Martin E. Hopkins, H. Peter Hofstee	
		FILING DATE Concurrently herewith	GROUP Art Unit

U.S. PATENT DOCUMENT

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>whe</i>	4 8 1 4 9 7 6	3/21/1989	Hansen et al.			

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>whe</i>	1. John L. Hennessy, "VLSI Processor Architecture," December 1984, IEEE Transactions On Computers, Vol. C-33, No. 12, pp. 1221-1246
<i>↓</i>	2. Richard L. Sites, "Alpha AXP Architecture," February 1993, Communications of the ACM, Vol. 36, No. 2, pp. 33 - 44
	3. Patent Application entitled "Processor Implementation having Unified Scalar and SIMD Datapath," Attorney Docket No.: YOR9-2001-0675, filed on August 14, 2001

EXAMINER <i>[Signature]</i>	DATE CONSIDERED <i>7/20/04</i>
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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